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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/629,673	07/30/2003	Hee Bok Kang	40296-0036	6550
26633	7590	09/20/2005	EXAMINER	
HELLER EHRMAN WHITE & MCAULIFFE LLP 1717 RHODE ISLAND AVE, NW WASHINGTON, DC 20036-3001			NGUYEN, VAN THU T	
			ART UNIT	PAPER NUMBER
			2824	
DATE MAILED: 09/20/2005				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/629,673

Applicant(s)

KANG, HEE BOK

Examiner

VanThu Nguyen

Art Unit

2824

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on AF on 7/13/05 and RCE on 8/11/05.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-9 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 3-9 is/are allowed.
- 6) ☒ Claim(s) 1-2 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 30 July 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
- 1) ☒ Certified copies of the priority documents have been received.
 - 2) ☐ Certified copies of the priority documents have been received in Application No. _____.
 - 3) ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>07/13/2005</u> . | 6) <input type="checkbox"/> Other: _____ |

Response to Amendment

1. This Office Action is in response to After Final Amendment filed on July 13, 2005 and Request for Continued Examination filed on August 11, 2005.
2. Claims 1-9 are pending.
3. Claims 10-20 are cancelled.

Response to Arguments

4. Applicant's arguments with respect to claims 1-2 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 103

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claims 1-2 are rejected under 35 U.S.C. 103(a) as being unpatentable over Heap (PGPub. 2004/0093457) in view of Gulpta et al. (U.S. Patent No. 6,405,286).

Regarding claims 1-2, Heap discloses an interleave control device using any type of memory device that is organized into blocks or banks (FeRAM fit into this category, see paragraph [0052]), comprising:

a memory array inherently formed in a single chip which controls access time differently in each address and include a plurality of single banks (addresses corresponding to each memory banks in memory array 903 which are operated in an interleave manner, see Fig. 9);

a memory interleave controller (memory controller 901, see Figs. 9-11) for differently controlling a memory interleave operation depending on input physical addresses, and to change an address path of the memory array (i.e. for selecting other banks beside current one); and

a bus configured to transfer data between the memory array and the memory interleave controller (data bus 907, see Fig. 9).

Heap further discloses that the memory interleave controller 901 control the interleave operation by mapping input physical addresses to logical addresses with the use of hardware. However, Heap also mentions that the procedure can be implemented with a processor readable medium using software with programming code essentially to perform the necessary tasks, wherein the processor readable medium (can be called registers) may include any medium that can store information, and nonvolatile ferroelectric memory is one of the kind (see paragraph [0054]).

However, Heap does not disclose that the memory interleaving operation is controlled depending on an access latency time and a restore latency time which are set in a memory interleave region corresponding to lower address bits of row address bits.

Gulpta et al. disclose, in FIG. 2, an interleave control device comprising a memory interleave controller (22, see FIG. 2) configured to program a code using inherent registers (in Interleaving Scheme LookUp Table 24) for differently controlling a memory interleave operation depending on an access latency time (i.e. row access time) and a restore latency time (i.e. precharge time) which are set in a memory interleave region corresponding to lower address bits

Art Unit: 2824

of row address bits in a memory (8 bits for DRAM Page Interleaving within 11 to 14 Row Bits in FIG. 5, also see column 9, lines 3-13).

Since Heap and Gulpta et al. are both from the same field of endeavor, the purpose disclosed by Gulpta et al. would have been recognized in the pertinent art of Heap.

It would have been obvious at the time the invention was made to a person having ordinary skill in the art to control the interleave operation depending on access latency time and restore latency time because that is one of well-known interleaving schemes called Page Interleaving.

Allowable Subject Matter

7. Claims 3-9 are allowed.

Conclusion

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VanThu Nguyen whose telephone number is (571) 272-1881. The examiner can normally be reached on Monday-Friday, 9:00am-5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR

Art Unit: 2824

system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

September 12, 2005

A handwritten signature in black ink, appearing to read "VanThu Nguyen". The signature is fluid and cursive, with the first name "VanThu" and last name "Nguyen" clearly distinguishable.

VanThu Nguyen
Primary Examiner
Art Unit 2824